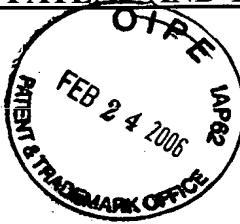


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re U.S. Patent Application of

SAITO et al

Application Number: 10/765,198

Filed: January 28, 2004

For: AN ACCUMULATION MODE CMOS
SEMICONDUCTOR DEVICE HAVING LOW
MOBILITY REDUCTION (AS AMENDED)

Attorney Docket No. HITA.0501

) Art Unit 2814

) Examiner Phat X. Cao

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**Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

This is in response to the Office Action mailed on August 24, 2005, the period of response to which is set to expire on February 24, 2006, with the petition and payment of the government fee for an extension of time of three months. Please amend the above-referenced application as follows: